



Filed:

FEBRUARY 12, 2002

For:

NOTCH FILTER FOR DC OFFSET REDUCTION IN RADIO-FREOUENCY APPARATUS AND ASSOCIATED METHODS

Serial No.:

-10/07**9**,099

Group Art Unit:

2681

Examiner:

UNKNOWN

Atty Dkt:

SILA:097

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Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in a first class, postage prepaid envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on the date below:

50-56-11

Name

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#### INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, it is respectfully requested that this Information Disclosure Statement be entered and the document(s) listed on attached Form PTO-1449 be considered by the Examiner and made of record.

In accordance with 37 C.F.R §§ 1.97(g),(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

The present Information Disclosure Statement is being filed prior to the receipt of a first Official Action reflecting an examination on the merits, and hence is believed to be timely filed in

accordance with 37 C.F.R. § 1.97(b). No fees are believed to be due in connection with the filing-of this Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21

be deemed necessary for any reason relating to these materials, the Commissioner is hereby

authorized to deduct said fees from Deposit Account No. 10-1205/SILA:097.

Per 37 CFR 1.98(d), no copies of references A1-A48, B1-B6 and C1-91 have been

provided, as copies of these references have been previously submitted to the Office in one or

more of co-pending U.S. Patent Application Serial Nos. 09/821,340 filed on March 29, 2001,

which is entitled "Digital Interface In Radio-Frequency Apparatus And Associated Methods" and

09/821,342 filed on March 29, 2001, which is entitled "Partitioned Radio-Frequency Apparatus

And Associated Methods" and which is relied upon by the present application for an earlier

effective filing date under 35 U.S.C. Section 120.

Applicant respectfully requests that the listed document(s) be made of record in the present

case.

Respectfully submitted,

Maximilian R. Peterson

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O'KEEFE, EGAN & PETERMAN, LLP 1101 Capital of Texas Highway South Building C, Suite 200 Austin, Texas 78746 (512) 347-1611

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**Enclosures** 

st of Patents and Publications for Applicant's

Atty. Docket No. SILA:097

Serial No. 10/075,099

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(Use several sheets if necessary)

Filing Date: 2/12/02

Group: 2681

U.S. Patent Documents
See Pages 1-3

Foreign Patent Documents See Pages 3

Other Art See Pages 3-10

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SILA:097

Applicants

Serial No.
10/075,099

TOD PAULUS ET AL.

**INFORMATION DISCLOSURE STATEMENT** 

(Use several sheets if necessary)

Filing Date: Group: 2/12/02 2681

U.S. Patent Documents
See Pages 1-3

Foreign Patent Documents
See Pages 3

Other Art See Pages 3-10

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Filing Date: Group: 2/12/02 2681

U.S. Patent Documents
See Pages 1-3

Foreign Patent Documents

See Pages 3

Other Art See Pages 3-10

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	B4	WO 00/11794	3/2/00	Moore et al.	***	BCIIIIOIGA	
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	Filing Date:	Group:	
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See Pages 1-3	See Pages 3	See Pages 3-10	

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(Use several sheets if necessary)

Filing Date: Group: 2/12/02 2681

U.S. Patent Documents See Pages 1-3

**Foreign Patent Documents** See Pages 3

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Serial No. 10/075,099

**Applicants** 

TOD PAULUS ET AL.

(Use several sheets if necessary) Filing Date: 2/12/02

Group: 2681

U.S. Patent Documents See Pages 1-3 Foreign Patent Documents

See Pages 3

Other Art See Pages 3-10

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Serial No. 10/075,099

**Applicants** 

TOD PAULUS ET AL.

**INFORMATION DISCLOSURE STATEMENT** 

(Use several sheets if necessary)

Filing Date: Group: 2/12/02 2681

U.S. Patent Documents

See Pages 1-3

Foreign Patent Documents

See Pages 3

Other Art See Pages 3-10

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Atty. Docket No. Serial No. SILA:097 10/075,099

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TOD PAULUS ET AL.

FORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Filing Date: Group: 2681 2/12/02

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U.S. Patent Documents See Pages 1-3

Foreign Patent Documents See Pages 3

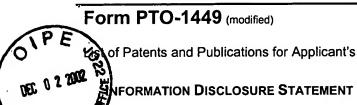
DEC 0 4 2002 Other Art See Pages 3-10
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Atty. Docket No. SILA:097

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Filing Date: 2/12/02

Group: 2681

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U.S. Patent Documents

See Pages 1-3

Foreign Patent Documents
See Pages 3

Official Andlogy Center 2600 See Pages 3-10

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Filing Date: 2/12/02

Group: 2681 **DEC 0 4 2002** 

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See Pages 1-3

Foreign Patent Documents

See Pages 3

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See Pages 3-10

## Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation		
	C87	Motorola Communications Semiconductor Product Division, "A 1.9 GHz Chipset For PCS Applications," Microwave Journal, No. 6, June 1995, 3 pgs.		
	C88	Search Report for PCT/US02/00896; October 4, 2002; 7 pgs.		
	C89	Copending U.S. Patent Application Serial No. 09/708,339, filed November 8, 2000, "Method And Apparatus For Operating A PLL With A Phase Detector/Sample Hold Circuit For Synthesizing High-Frequency Signals For Wireless Communications" (Sila:035C1)		
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